

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
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You Hie HAN et al.)	Group Art Unit: Unassigned
)	
Application No.: Unassigned)	Examiner: Unassigned
)	
Filed: July 10, 2003)	Confirmation No.: Unassigned
)	
For: CHIP SCALE MARKER AND)	
METHOD OF CALIBRATING)	
MARKING POSITION)	
)	
)	

CLAIM FOR CONVENTION PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The benefit of the filing date of the following prior foreign application in the following foreign country is hereby requested, and the right of priority provided in 35 U.S.C. § 119 is hereby claimed:

Korean Patent Application No. 2002-83202

Filed: December 24, 2002

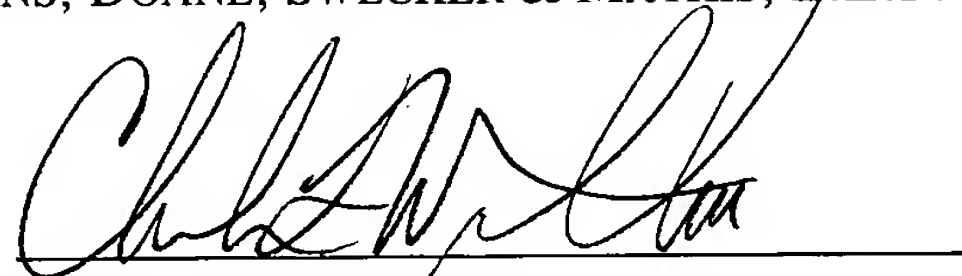
In support of this claim, enclosed is a certified copy of said prior foreign application. Said prior foreign application was referred to in the oath or declaration. Acknowledgment of receipt of the certified copy is requested.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: July 10, 2003

By:



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KOREAN INTELLECTUAL PROPERTY OFFICE

This is to certify that the following application annexed hereto is a true copy from the records of the Korean Intellectual Property Office.

Application Number: Korean Patent 2002-0083202

Date of Application: 24 December 2002

Applicant(s): EO Technics Co., Ltd.

21 February 2003

COMMISSIONER

[Bibliography]

[Document Name] Patent Application
[Classification] Patent
[Receiver] Commissioner
[Reference No.] 0001
[Filing Date] 24 December 2002
[IPC] H01S

[Title] Chip scale marker and method of calibrating the marking position therewith

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[Purpose] We file as above according to Art. 42 of the Patent Law.
Attorney Youngpil Lee
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[Fee]		
[Basic page]	20 Sheet(s)	29,000 won
[Additional page]	11 Sheet(S)	11,000 won
[Priority claiming fee]	0 Case(S)	0 won
[Examination fee]	0 Claim(s)	0 won
[Total]	40,000 won	

[Enclosures]

1. Abstract and Specification (and Drawings)	1 copy
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